

INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

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APPLICANT(S):
RICHARD E. FACKENTHAL

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October 16, 2003

GROUP ART UNIT:
2133

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
GL	A.	6,549,457	04/15/2003	SRINIVASAN ET AL.	365	185.03	
	B.	US2003/0053333	03/20/2003	RUDELIC ET AL.	365	185.03	
	C.						
	D.						
	E.						
	F.						
	G.						
	H.						
	I.						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	J.							
	K.							
	L.							
	M.							
	N.							
	O.							
	P.							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Q.	Construction of Polyvalent Error Control Codes for Multilevel Memories; Stefano Gregori, Pietro Ferrari, Rino Micheloni and Guido Torelli; pp. 751-754; 2000
GL	R.	An Error Control Code Scheme for Multilevel Flash Memories; Stefano Gregori, Osama Khouri, Rino Micheloni and Guido Torelli; pp. 45-49; 2001
	S.	Using Non-Volatile Memories for Disk Caching; John I. Garney; U.S. Patent Application Serial No.: 10/287,115; filed: November 4, 2002
	T.	
	U.	
	V.	
	W.	

EXAMINER /Guy Lamarre/ (05/30/2006)

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.